

FIG. 1

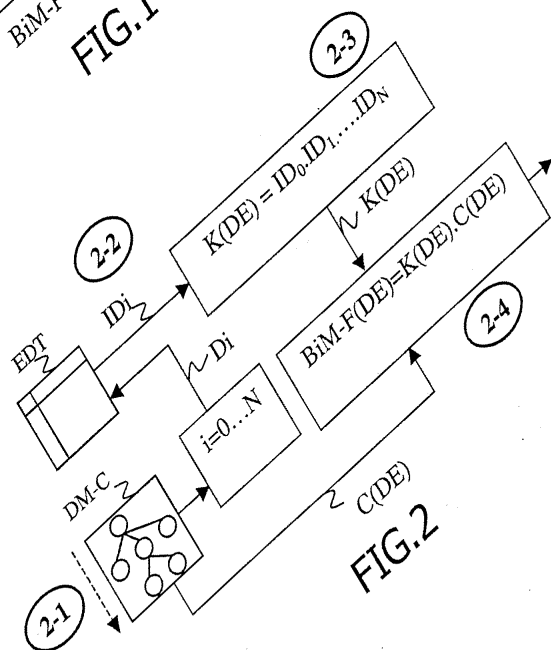


FIG. 2

03902253 \* 101731

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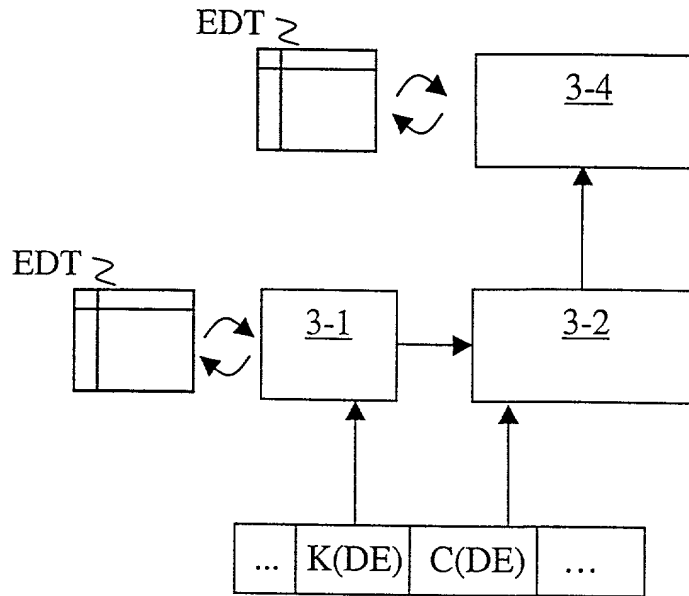


FIG.3

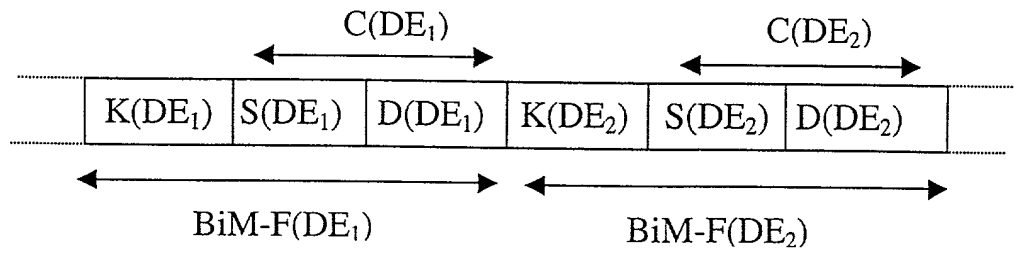


FIG.4

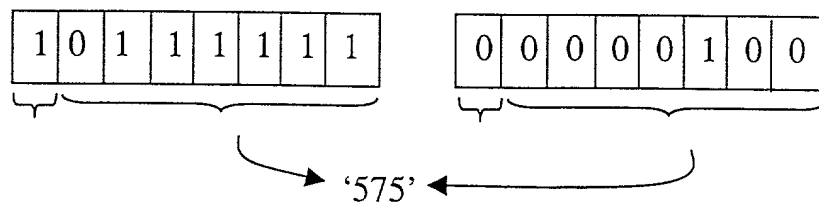


FIG.6

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Diagram illustrating the iterative construction of a Gray code sequence for 3 bits. The sequence is built by shifting the previous sequence right by one bit and adding a new bit.

- Initial state: Two registers, each containing eight zeros. The first register is labeled '0'.
- First iteration: The second register is shifted right by one bit, resulting in a sequence of seven zeros followed by a one. The first register remains '0'.
- Second iteration: The first register is shifted right by one bit, resulting in a sequence of six zeros followed by two ones. The second register remains '0'.
- Third iteration: The second register is shifted right by one bit, resulting in a sequence of five zeros followed by three ones. The first register remains '0'.

FIG.5